

LM6142/LM6144

17 MHz Rail-to-Rail Input-Output Operational Amplifiers

General Description

Using patent pending new circuit topologies, the LM6142/LM6144 provides new levels of performance in applications where low voltage supplies or power limitations previously made compromise necessary. Operating on supplies of 1.8V to over 24V, the LM6142/LM6144 is an excellent choice for battery operated systems, portable instrumentation and others.

The greater than rail-to-rail input voltage range eliminates concern over exceeding the common-mode voltage range. The rail-to-rail output swing provides the maximum possible dynamic range at the output. This is particularly important when operating on low supply voltages.

High gain-bandwidth with 650 μ A/Amplifier supply current opens new battery powered applications where previous higher power consumption reduced battery life to unacceptable levels. The ability to drive large capacitive loads without oscillating functionally removes this common problem.

Features

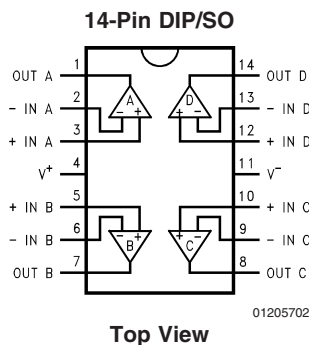
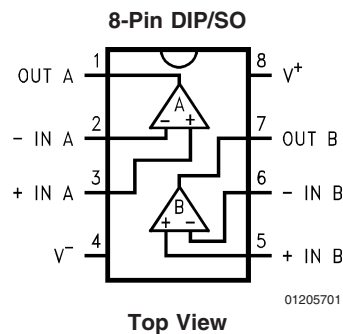
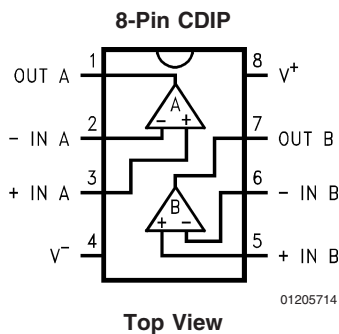
At $V_S = 5V$. Typ unless noted.

- Rail-to-rail input CMVR $-0.25V$ to $5.25V$
- Rail-to-rail output swing $0.005V$ to $4.995V$
- Wide gain-bandwidth: 17MHz at 50kHz (typ)
- Slew rate:
 - Small signal, 5V/ μ s
 - Large signal, 30V/ μ s
- Low supply current 650 μ A/Amplifier
- Wide supply range 1.8V to 24V
- CMRR 107dB
- Gain 108dB with $R_L = 10k$
- PSRR 87dB

Applications

- Battery operated instrumentation
- Depth sounders/fish finders
- Barcode scanners
- Wireless communications
- Rail-to-rail in-out instrumentation amps

Connection Diagrams



Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

ESD Tolerance (Note 2)	2500V
Differential Input Voltage	15V
Voltage at Input/Output Pin	(V ⁺) + 0.3V, (V ⁻) - 0.3V
Supply Voltage (V ⁺ - V ⁻)	35V
Current at Input Pin	±10mA
Current at Output Pin (Note 3)	±25mA
Current at Power Supply Pin	50mA
Lead Temperature	
(soldering, 10 sec)	260°C
Storage Temp. Range	-65°C to +150°C
Junction Temperature (Note 4)	150°C

Operating Ratings (Note 1)

Supply Voltage	1.8V ≤ V ⁺ ≤ 24V
Temperature Range	
LM6142, LM6144	-40°C ≤ T _A ≤ +85°C
Thermal Resistance (θ _{JA})	
N Package, 8-Pin Molded DIP	115°C/W
M Package, 8-Pin Surface Mount	193°C/W
N Package, 14-Pin Molded DIP	81°C/W
M Package, 14-Pin Surface Mount	126°C/W

5.0V DC Electrical Characteristics (Note 8)

Unless otherwise specified, all limits guaranteed for T_A = 25°C, V⁺ = 5.0V, V⁻ = 0V, V_{CM} = V_O = V⁺/2 and R_L > 1 MΩ to V⁺/2. **Boldface limits** apply at the temperature extremes.

Symbol	Parameter	Conditions	Typ (Note 5)	LM6144AI LM6142AI Limit (Note 6)	LM6144BI LM6142BI Limit (Note 6)	Units
V _{OS}	Input Offset Voltage		0.3	1.0 2.2	2.5 3.3	mV max
TCV _{OS}	Input Offset Voltage Average Drift		3			μV/°C
I _B	Input Bias Current		170	250	300	nA max
		0V ≤ V _{CM} ≤ 5V	180	280 526	300 526	nA max
I _{OS}	Input Offset Current		3	30 80	30 80	nA max
R _{IN}	Input Resistance, C _M		126			MΩ
CMRR	Common Mode Rejection Ratio	0V ≤ V _{CM} ≤ 4V	107	84 78	84 78	dB min
		0V ≤ V _{CM} ≤ 5V	82 79	66 64	66 64	
PSRR	Power Supply Rejection Ratio	5V ≤ V ⁺ ≤ 24V	87	80 78	80 78	
V _{CM}	Input Common-Mode Voltage Range		-0.25 5.25	0 5.0	0 5.0	V
A _V	Large Signal Voltage Gain	R _L = 10k	270 70	100 33	80 25	V/mV min
V _O	Output Swing	R _L = 100k	0.005 4.995	0.01 4.98 0.013 4.93	0.01 4.98 0.013 4.93	V max V min
		R _L = 10k	0.02 4.97			V max V min
		R _L = 2k	0.06 4.90	0.1 0.133 4.86	0.1 0.133 4.86	V max V

5.0V DC Electrical Characteristics (Note 8) (Continued)

Unless otherwise specified, all limits guaranteed for $T_A = 25^\circ\text{C}$, $V^+ = 5.0\text{V}$, $V^- = 0\text{V}$, $V_{\text{CM}} = V_O = V^+/2$ and $R_L > 1\text{ M}\Omega$ to $V^+/2$. **Boldface limits** apply at the temperature extremes.

Symbol	Parameter	Conditions	Typ (Note 5)	LM6144AI LM6142AI Limit (Note 6)	LM6144BI LM6142BI Limit (Note 6)	Units
				4.80	4.80	min
I_{SC}	Output Short Circuit Current LM6142	Sourcing	13	10	8	mA
				4.9	4	min
		Sinking	24	10	10	mA
				5.3	5.3	min
			35	35	mA max	
I_{SC}	Output Short Circuit Current LM6144	Sourcing	8	6	6	mA
				3	3	min
		Sinking	22	8	8	mA
				4	4	min
			35	35	mA max	
I_{S}	Supply Current	Per Amplifier	650	800 880	800 880	μA max

5.0V AC Electrical Characteristics (Note 8)

Unless Otherwise Specified, All Limits Guaranteed for $T_A = 25^\circ\text{C}$, $V^+ = 5.0\text{V}$, $V^- = 0\text{V}$, $V_{\text{CM}} = V_O = V^+/2$ and $R_L > 1\text{ M}\Omega$ to $V^+/2$. **Boldface limits** apply at the temperature extremes.

Symbol	Parameter	Conditions	Typ (Note 5)	LM6144AI LM6142AI Limit (Note 6)	LM6144BI LM6142BI Limit (Note 6)	Units
SR	Slew Rate	$8 V_{\text{PP}} @ V^+ 12\text{V}$ $R_S > 1\text{ k}\Omega$	25	15 13	13 11	V/ μs min
GBW	Gain-Bandwidth Product	$f = 50\text{ kHz}$	17	10 6	10 6	MHz min
ϕ_m	Phase Margin		38			Deg
	Amp-to-Amp Isolation		130			dB
e_n	Input-Referred Voltage Noise	$f = 1\text{ kHz}$	16			$\frac{\text{nV}}{\sqrt{\text{Hz}}}$
i_n	Input-Referred Current Noise	$f = 1\text{ kHz}$	0.22			$\frac{\text{pA}}{\sqrt{\text{Hz}}}$
T.H.D.	Total Harmonic Distortion	$f = 10\text{ kHz}$, $R_L = 10\text{ k}\Omega$,	0.003			%

2.7V DC Electrical Characteristics (Note 8)

Unless Otherwise Specified, All Limits Guaranteed for $T_A = 25^\circ\text{C}$, $V^+ = 2.7\text{V}$, $V^- = 0\text{V}$, $V_{\text{CM}} = V_O = V^+/2$ and $R_L > 1\text{M}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extreme

Symbol	Parameter	Conditions	Typ (Note 5)	LM6144AI LM6142AI Limit (Note 6)	LM6144BI LM6142BI Limit (Note 6)	Units
V_{OS}	Input Offset Voltage		0.4	1.8 4.3	2.5 5	mV max
I_{B}	Input Bias Current		150	250 526	300 526	nA max
I_{OS}	Input Offset Current		4	30 80	30 80	nA max
R_{IN}	Input Resistance		128			$\text{M}\Omega$
CMRR	Common Mode Rejection Ratio	$0\text{V} \leq V_{\text{CM}} \leq 1.8\text{V}$	90			dB min
		$0\text{V} \leq V_{\text{CM}} \leq 2.7\text{V}$	76			
PSRR	Power Supply Rejection Ratio	$3\text{V} \leq V^+ \leq 5\text{V}$	79			
V_{CM}	Input Common-Mode Voltage Range		-0.25	0	0	V min
			2.95	2.7	2.7	V max
A_{V}	Large Signal Voltage Gain	$R_L = 10\text{k}$	55			V/mV min
V_{O}	Output Swing	$R_L = 100\text{k}\Omega$	0.019	0.08 0.112	0.08 0.112	V max
			2.67	2.66 2.25	2.66 2.25	V min
I_{S}	Supply Current	Per Amplifier	510	800 880	800 880	μA max

2.7V AC Electrical Characteristics (Note 8)

Unless Otherwise Specified, All Limits Guaranteed for $T_A = 25^\circ\text{C}$, $V^+ = 2.7\text{V}$, $V^- = 0\text{V}$, $V_{\text{CM}} = V_O = V^+/2$ and $R_L > 1\text{M}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extreme

Symbol	Parameter	Conditions	Typ (Note 5)	LM6144AI LM6142AI Limit (Note 6)	LM6144BI LM6142BI Limit (Note 6)	Units
GBW	Gain-Bandwidth Product	$f = 50\text{kHz}$	9			MHz
ϕ_{m}	Phase Margin		36			Deg
G_{m}	Gain Margin		6			dB

24V Electrical Characteristics (Note 8)

Unless Otherwise Specified, All Limits Guaranteed for $T_A = 25^\circ\text{C}$, $V^+ = 24\text{V}$, $V^- = 0\text{V}$, $V_{\text{CM}} = V_O = V^+/2$ and $R_L > 1\text{ M}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extreme

Symbol	Parameter	Conditions	Typ (Note 5)	LM6144AI LM6142AI Limit (Note 6)	LM6144BI LM6142BI Limit (Note 6)	Units
V_{OS}	Input Offset Voltage		1.3	2 4.8	3.8 4.8	mV max
I_{B}	Input Bias Current		174			nA max
I_{OS}	Input Offset Current		5			nA max
R_{IN}	Input Resistance		288			$\text{M}\Omega$
CMRR	Common Mode Rejection Ratio	$0\text{V} \leq V_{\text{CM}} \leq 23\text{V}$	114			dB min
		$0\text{V} \leq V_{\text{CM}} \leq 24\text{V}$	100			
PSRR	Power Supply Rejection Ratio	$0\text{V} \leq V_{\text{CM}} \leq 24\text{V}$	87			
V_{CM}	Input Common-Mode Voltage Range		-0.25	0	0	V min
			24.25	24	24	V max
A_{V}	Large Signal Voltage Gain	$R_L = 10\text{k}$	500			V/mV min
V_{O}	Output Swing	$R_L = 10\text{ k}\Omega$	0.07	0.15 0.185	0.15 0.185	V max
			23.85	23.81 23.62	23.81 23.62	V min
I_{S}	Supply Current	Per Amplifier	750	1100 1150	1100 1150	μA max
GBW	Gain-Bandwidth Product	$f = 50\text{ kHz}$	18			MHz

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics.

Note 2: Human body model, 1.5k Ω in series with 100pF.

Note 3: Applies to both single-supply and split-supply operation. Continuous short circuit operation at elevated ambient temperature can result in exceeding the maximum allowed junction temperature of 150°C.

Note 4: The maximum power dissipation is a function of $T_{\text{J(MAX)}}$, θ_{JA} , and T_{A} . The maximum allowable power dissipation at any ambient temperature is $P_{\text{D}} = (T_{\text{J(MAX)}} - T_{\text{A}})/\theta_{\text{JA}}$. All numbers apply for packages soldered directly into a PC board.

Note 5: Typical values represent the most likely parametric norm.

Note 6: All limits are guaranteed by testing or statistical analysis.

Note 7: For guaranteed military specifications see military datasheet MNLM6142AM-X.

Note 8: Electrical Table values apply only for factory testing conditions at the temperature indicated. Factory testing conditions result in very limited self-heating of the device such that $T_{\text{J}} = T_{\text{A}}$. No guarantee of parametric performance is indicated in the electrical tables under conditions of the internal self heating where $T_{\text{J}} > T_{\text{A}}$.

Typical Applications (Continued)

lowers the input impedance. Any mismatch in these precision resistors reduces the CMR as well. Using the LM6144, all of these problems are eliminated.

In this example, amplifiers A and B act as buffers to the differential stage (*Figure 6*). These buffers assure that the input impedance is over 100M Ω and they eliminate the requirement for precision matched resistors in the input stage. They also assure that the difference amp is driven from a voltage source. This is necessary to maintain the CMR set by the matching of R1–R2 with R3–R4.

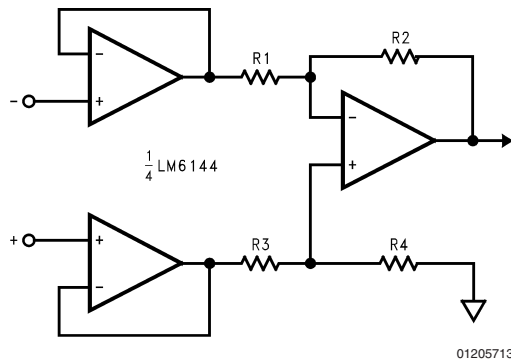


FIGURE 6.

Ordering Information

Package	Temperature Range	Temperature Range	NSC Drawing
	Industrial –40°C to +85°C	Military –55°C to +125°C	
8-Pin Molded DIP	LM6142AIN		N08E
	LM6142BIN		
8-Pin Small Outline	LM6142AIM		M08A
	LM6142AIMX		
	LM6142BIM		
	LM6142BIMX		
14-Pin Molded DIP	LM6144AIN		N14A
	LM6144BIN		
14-Pin Small Outline	LM6144AIM		M14A
	LM6144AIMX		
	LM6144BIM		
	LM6144BIMX		
8-Pin CDIP		LM6142AMJ-QML	J08A

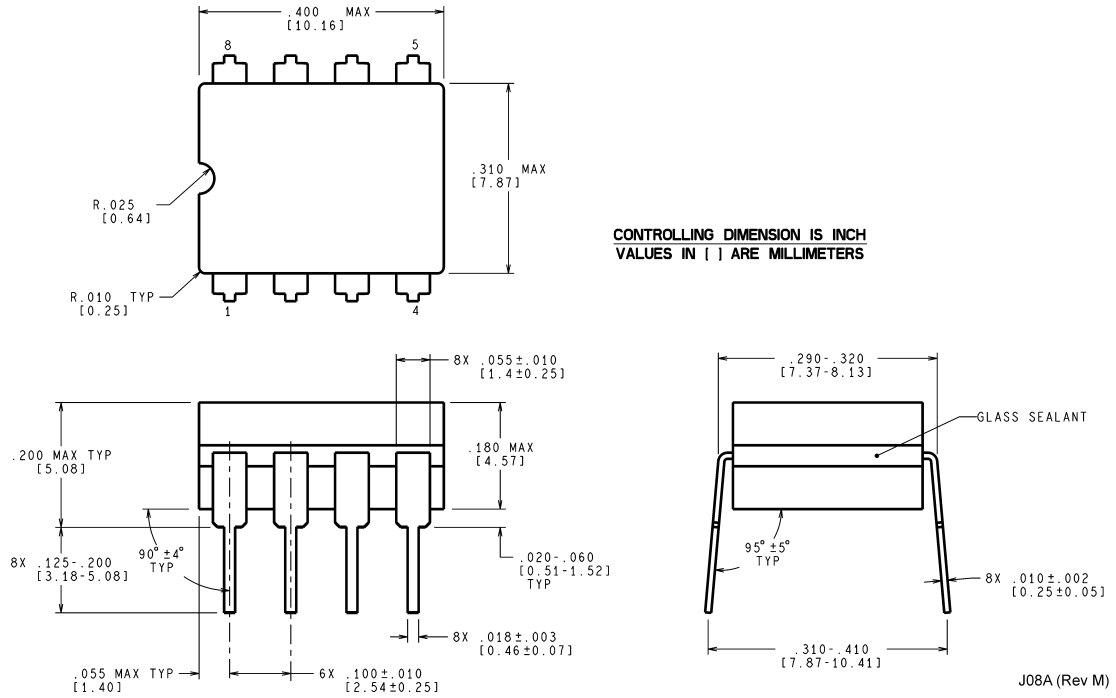
The gain is set by the ratio of R2/R1 and R3 should equal R1 and R4 equal R2. Making R4 slightly smaller than R2 and adding a trim pot equal to twice the difference between R2 and R4 will allow the CMR to be adjusted for optimum.

With both rail to rail input and output ranges, the inputs and outputs are only limited by the supply voltages. Remember that even with rail-to-rail output, the output can not swing past the supplies so the combined common mode voltage plus the signal should not be greater than the supplies or limiting will occur.

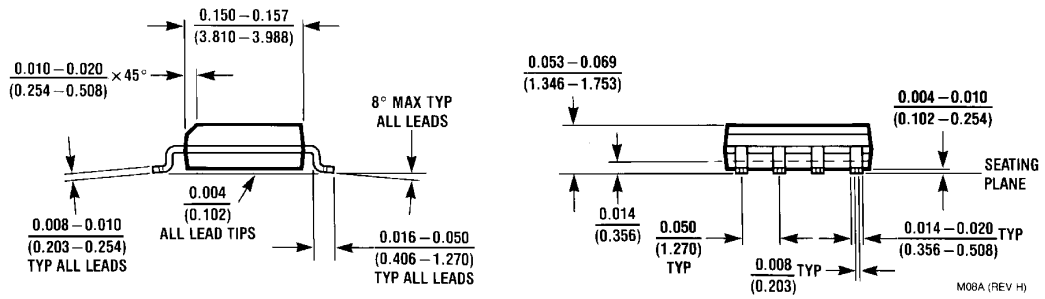
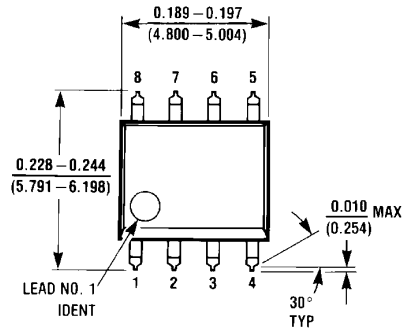
SPICE MACROMODEL

A SPICE macromodel of this and many other National Semiconductor op amps is available at no charge from the NSC Customer Response Group at 800-272-9959.

Physical Dimensions inches (millimeters)
 unless otherwise noted

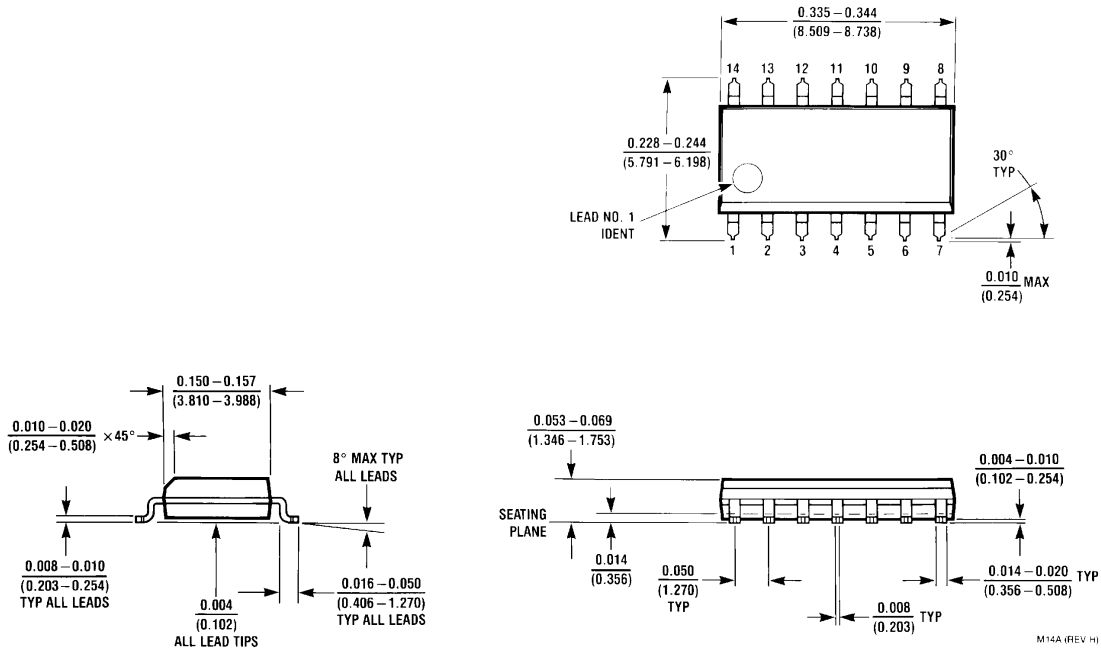


**8-Pin Cerdip
 Dual-In-Line Package
 NS Package Number J08A**

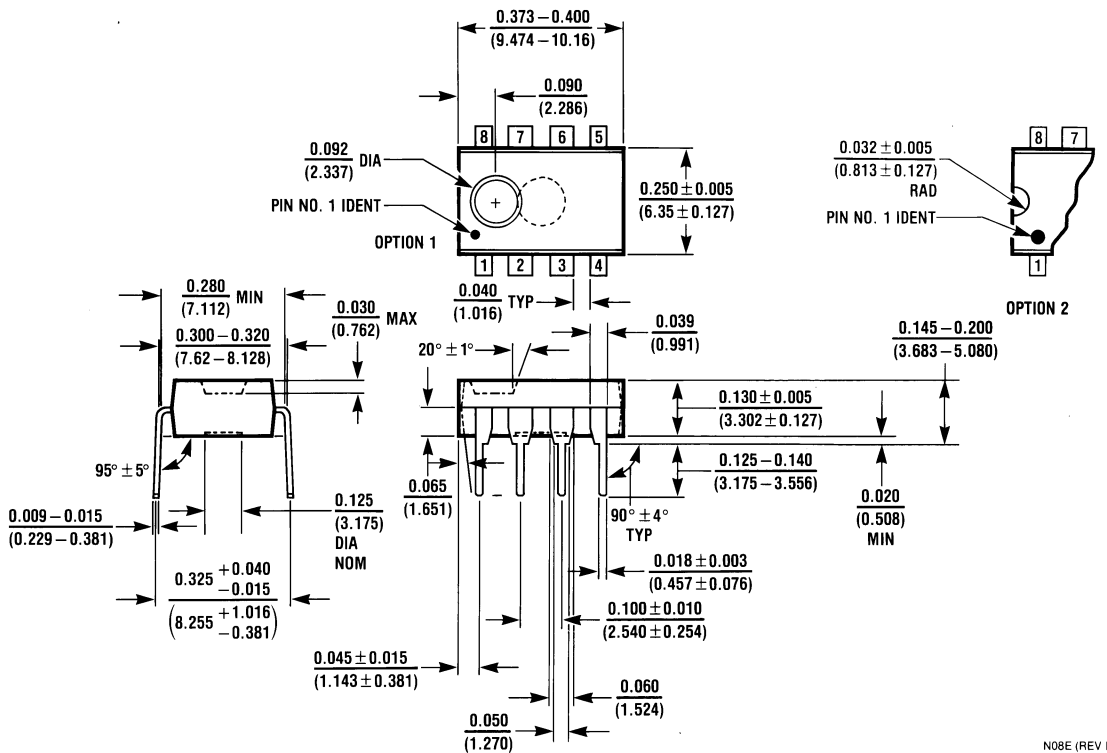


**8-Pin Small Outline Package
 NS Package Number M08A**

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



14-Pin Small Outline Package
NS Package Number M14A



8-Pin Molded Dual-In-Line Package
NS Package Number N08E